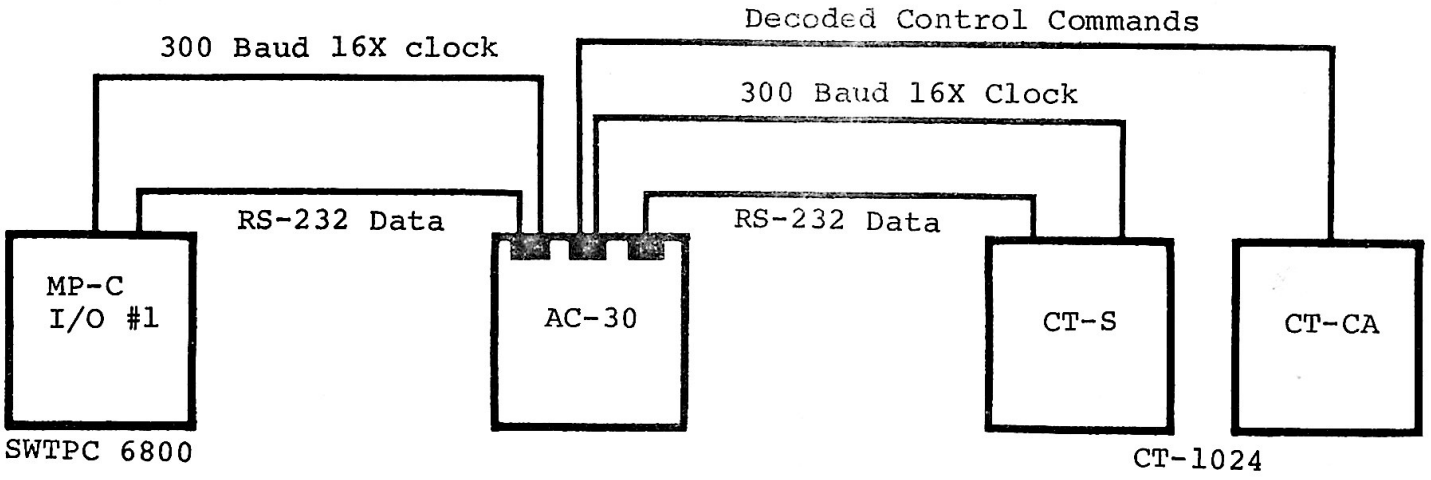
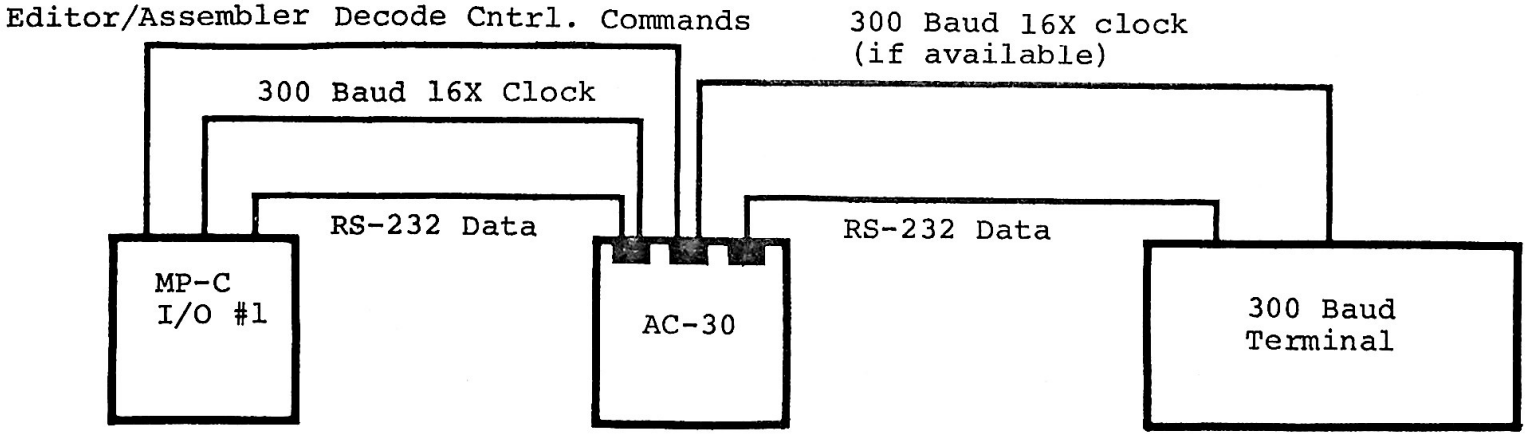
INTERCONNECTION BLOCK DIAGRAMS

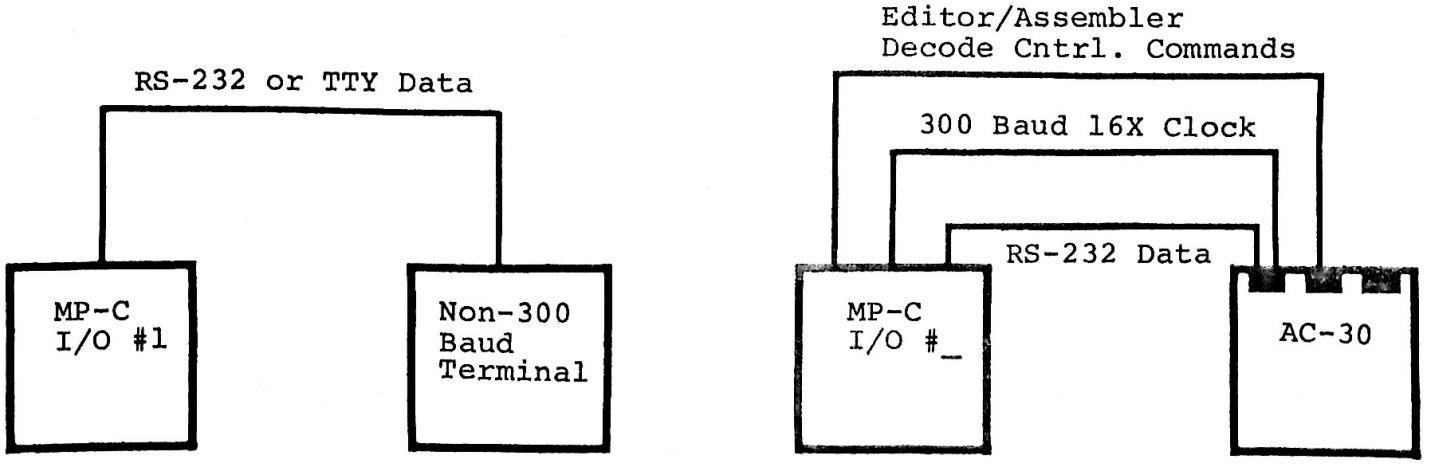
SWTPC 6800 Computer System /AC-30 /CT-1024 Terminal System



SWTPC 6800 Computer System /AC-30 /NON-CT-1024 300 Baud Terminal System



SWTPC 6800 Computer System /AC-30 /NON-300 Baud Terminal System



by IC13. Convenient front panel LED indicators always show the current status of these latches.

The multitude of data and clock switching is performed by six data selectors within IC6 and IC14. NAND gates IC9A and IC9B inhibit data and clock flow out to the cassette recorders unless the read latch is set and valid recorder carrier data is detected. Timer IC10 provides the adjustable $\overline{\text{CARRIER ENABLE}}$ delay needed when using the system for recording in the incremental (start-stop) mode All 300 baud data communication with the cassette interface is done thru integrated EIA RS-232 receiver and transmitter integrated circuits IC12 and IC15 respectively. Motor control for each recorder is provided by reed relays RLY1 and RLY2.